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**EXPEDITED PROCEDURE - EXAMINING GROUP 2812**

PATENT

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Alan R. Reinberg

Examiner: Richard A. Booth

Serial No.: 09/382,442

Group Art Unit: 2812

Filed: August 25, 1999

Docket: 303.522US1

Title: METHOD FOR REDUCING SINGLE BIT DATA LOSS IN A MEMORY  
CIRCUIT

**SUPPLEMENTAL AMENDMENT & RESPONSE UNDER 1.116**

Box RCE  
Commissioner for Patents  
Washington, D.C. 20231

In response to the Advisory Action mailed June 19, 2002, please amend the application as follows:

**IN THE CLAIMS**

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. The substitute claim set is intended to reflect amendment of previously pending claims 1, 26, and 37. The specific amendment to the individual claim is detailed in the following marked up set of claims.

1. (Amended) A method for reducing random single bit data loss in a FLASH memory circuit comprising:

providing a semiconductor layer having a surface;

heating the layer in an atmosphere comprising a Hydrogen isotope wherein the Hydrogen isotope is incorporated into the layer; and

fabricating a memory circuit having a programming operation and an erase operation, comprising single bit data using the semiconductor layer wherein single bit data loss is reduced and wherein random single bit data loss is prevented in both the programming operation and the erase operation..